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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	JAPAN	4	20	3
Verified and Acknowledged	<i>M. M.</i> Examiner's Signature <i>M. M.</i> Initials				

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TITLE

Semiconductor memory device and manufacturing method thereof

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